

Refine Search

Search Results -

Term	Documents
(10 AND 14).PGPB,USPT.	10
(L14 AND L10).PGPB,USPT.	10

Database:

US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US OCR Full-Text Database
 EPO Abstracts Database
 JPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Search:

L15

Refine Search

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Search History

DATE: Wednesday, August 11, 2004 [Printable Copy](#) [Create Case](#)

<u>Set</u> <u>Name</u> side by side	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>			
<u>L15</u>	L14 and l10	10	<u>L15</u>
<u>L14</u>	(711/200-221)! [CCLS]	5033	<u>L14</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L13</u>	L11 and (identif\$6 or select\$6 or switch\$6) near4 mode\$1	25	<u>L13</u>
<u>L12</u>	L11 and (identif\$6 or select\$6 or switch\$6 or mode\$1)	73	<u>L12</u>
<u>L11</u>	L10 and l6	73	<u>L11</u>
<u>L10</u>	L9 and instruction near1 set	233	<u>L10</u>
<u>L9</u>	(extend\$3 or extension\$1 or mmx) near6 (select\$4 or switch\$6) and prefix\$3	2305	<u>L9</u>
<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>			
<u>L8</u>	L7 not l6	0	<u>L8</u>
<u>L7</u>	L6 and l4	12	<u>L7</u>

<u>L6</u>	(712/2-300)![CCLS]	10130	<u>L6</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L5</u>	L4 and instruction\$1 near2 set	18	<u>L5</u>
<u>L4</u>	(extend\$3 or extension\$1 or mmx) near6 (select\$4 or switch\$6) near25 prefix\$3	70	<u>L4</u>
<u>L3</u>	L2 and instruction\$1 near2 set	9	<u>L3</u>
<u>L2</u>	L1 near15 standard	27	<u>L2</u>
<u>L1</u>	(extend\$3 or extension\$1 or mmx) near18 prefix\$3	996	<u>L1</u>

END OF SEARCH HISTORY

Refine Search

Search Results -

Term	Documents
(24 AND 4).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	12
(L24 AND L4).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	12

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Search:

L25

Refine Search

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Search History

DATE: Friday, August 13, 2004 [Printable Copy](#) [Create Case](#)

<u>Set Name</u> side by side	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u> result set
	<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>		
<u>L25</u>	L24 and l4	12	<u>L25</u>
<u>L24</u>	L21 and (extend\$3 or extension\$1) near15 general near1 purpose near3 register\$1	16	<u>L24</u>
<u>L23</u>	L21 and (extend\$3 or extension\$1)	189	<u>L23</u>
<u>L22</u>	L21 and l4	119	<u>L22</u>
<u>L21</u>	(intel or microprocessor\$1) near4 architecture\$1 and l18	206	<u>L21</u>
<u>L20</u>	L19 and l2	254	<u>L20</u>
<u>L19</u>	L18 and (extend\$3 or extension\$1)	674	<u>L19</u>
<u>L18</u>	L1 and register\$1 near5 (field\$1 or portion\$1 or bit\$1) near5 (select\$5 or designat\$6 or indicat\$4 or chose\$1 or choos\$3)	862	<u>L18</u>
<u>L17</u>	L1 and (field\$1 or portion\$1 or bit\$1) near5 (select\$5 or designat\$6 or indicat\$4 or chose\$1 or choos\$3)	1361	<u>L17</u>

<u>L16</u>	L13 and l2	126	<u>L16</u>
<u>L15</u>	L13 and l4	139	<u>L15</u>
<u>L14</u>	L13 and l7	40	<u>L14</u>
<u>L13</u>	L1 and (field\$1 or portion\$1 or bit\$1) near5 instruction\$1 near5 format\$ <i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>	248	<u>L13</u>
<u>L12</u>	l1 and l7	245	<u>L12</u>
<u>L11</u>	l1 and l6	20	<u>L11</u>
<u>L10</u>	l1 and l5	4	<u>L10</u>
<u>L9</u>	l1 and l4	502	<u>L9</u>
<u>L8</u>	l1 and l2	466	<u>L8</u>
<u>L7</u>	(711/112-221)![CCLS]	18849	<u>L7</u>
<u>L6</u>	(712/229)[CCLS]	249	<u>L6</u>
<u>L5</u>	(712/203)[CCLS]	106	<u>L5</u>
<u>L4</u>	(712/43-300)[CCLS]	7524	<u>L4</u>
<u>L3</u>	(712/207-300)[CCLS]	6875	<u>L3</u>
<u>L2</u>	(712/207-300)![CCLS] <i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>	6875	<u>L2</u>
<u>L1</u>	(x86 or \$86 or 386 or 8086 or 286) near12 (standard or general near1 purpose or extend\$3 or extension\$1) near4 register\$1	2381	<u>L1</u>

END OF SEARCH HISTORY